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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/046,446	01/16/2002	Toshio Miyamoto	T&A-110	4041
7590	08/05/2004		EXAMINER	
MATTINGLY, STANGER & MALUR, P.C.			OWENS, DOUGLAS W	
1800 Diagonal Road, Suite 370			ART UNIT	PAPER NUMBER
Alexandria, VA 22314			2811	

DATE MAILED: 08/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.



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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
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10/046,446 1/16/02 Miyamoto, Toshiro T&A-110

EXAMINER

Douglas W. Owens

ART UNIT	PAPER
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2811 20040730

DATE MAILED:

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Commissioner for Patents

In reply to Applicant's request for a corrected Notice of Allowance on the grounds that the title should read "Wafer Level Chip Size Package Having Rerouting Layers", no corrected Notice of Allowance will be sent because this is already the title on record.

In response to the request for a copy of the 1449 filed January 16, 2002, an initialed copy was mailed with the Office Action of November 5, 2003. However, another copy is attached for Applicant's convenience.

Douglas W. Owens